



M/o/Vfuscator-Be-Gone

Recovering from soul-crushing RE nightmares

Julian Kirsch & Clemens Jonischkeit

Technical University of Munich

June 19, 2016

Disclaimer

1. This talk is **not** an attack on Christopher Domas
2. Our demovfuscator currently performs resubstitution **very sparsely**



#21 in BkP16, #6 in Insomni'hack16, #12 in pCTF16,
currently #18 in ctftime

Julian

- ▶ PhD student at TUM
- ▶ program analysis, (de-)obfuscation, malware analysis, ...

Clemens

- ▶ Master's student at TUM
- ▶ Heap exploitation guru
- ▶ Bachelor's thesis on demovfuscation

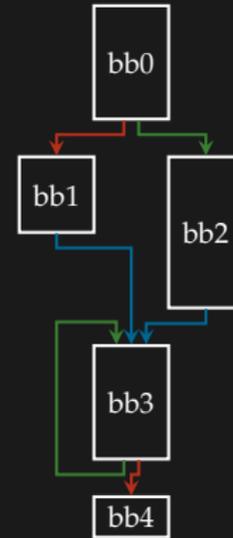
Chapter 1

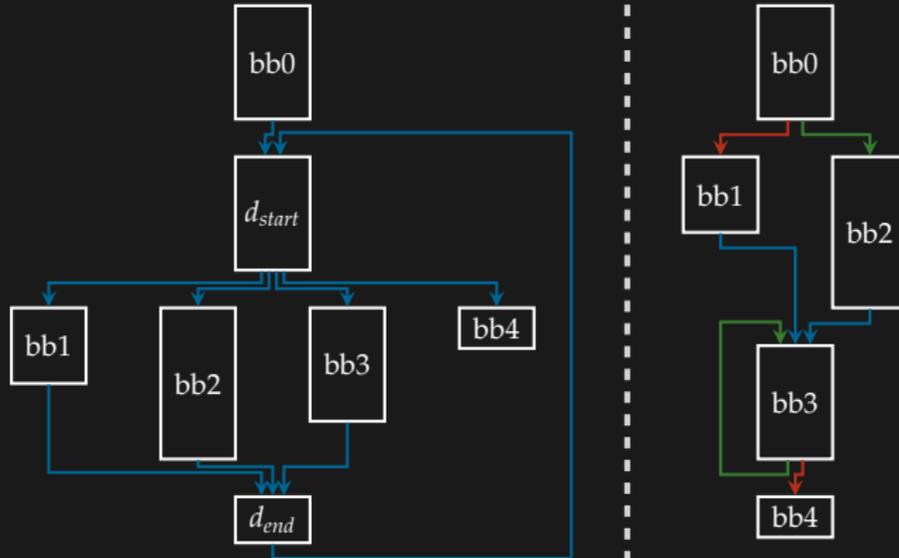
The Movfuscator

- ▶ mov is turing complete
- ▶ one-instruction compiler (1cc)
- ▶ VM implemented in mov
- ▶ two flavours:
 1. movfuscator1: BF \rightarrow x86/mov
 2. movfuscator2: C \rightarrow x86/mov

 **mov is Turing Complete.** Stephen Dolan. White Paper.
<https://www.cl.cam.ac.uk/~sd601/papers/mov.pdf>. 2013.

 **The M/o/Vfuscator.** Christopher Domas. REcon 2015.
<https://github.com/xoreaxeaxeax/movfuscator>. 2015

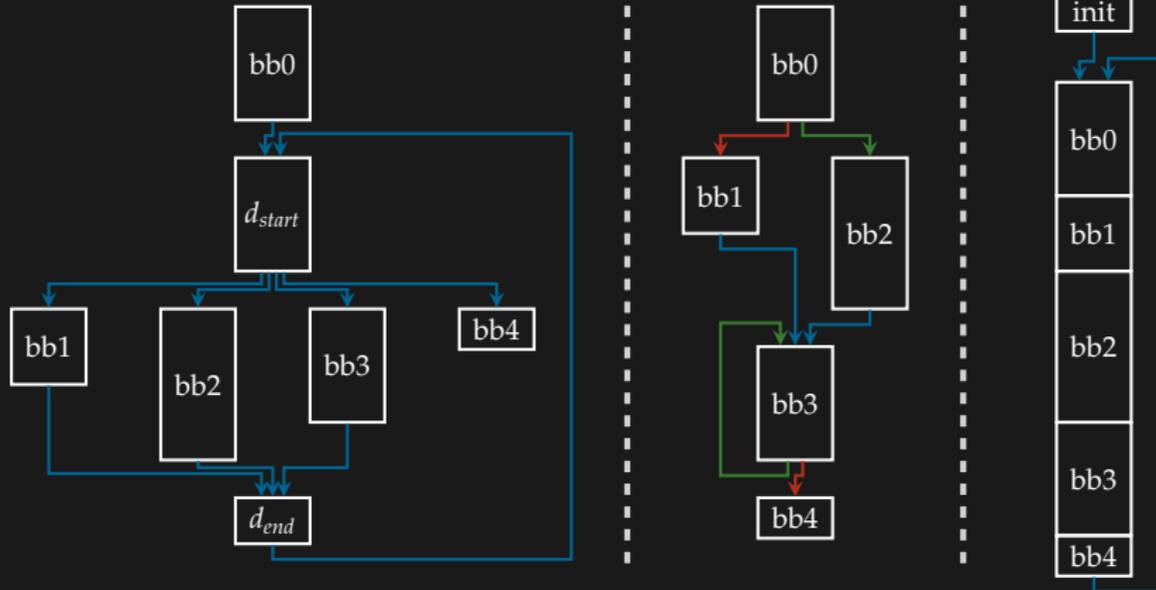




Control Flow Flattening



Control Flow Linearization



Control Flow Flattening

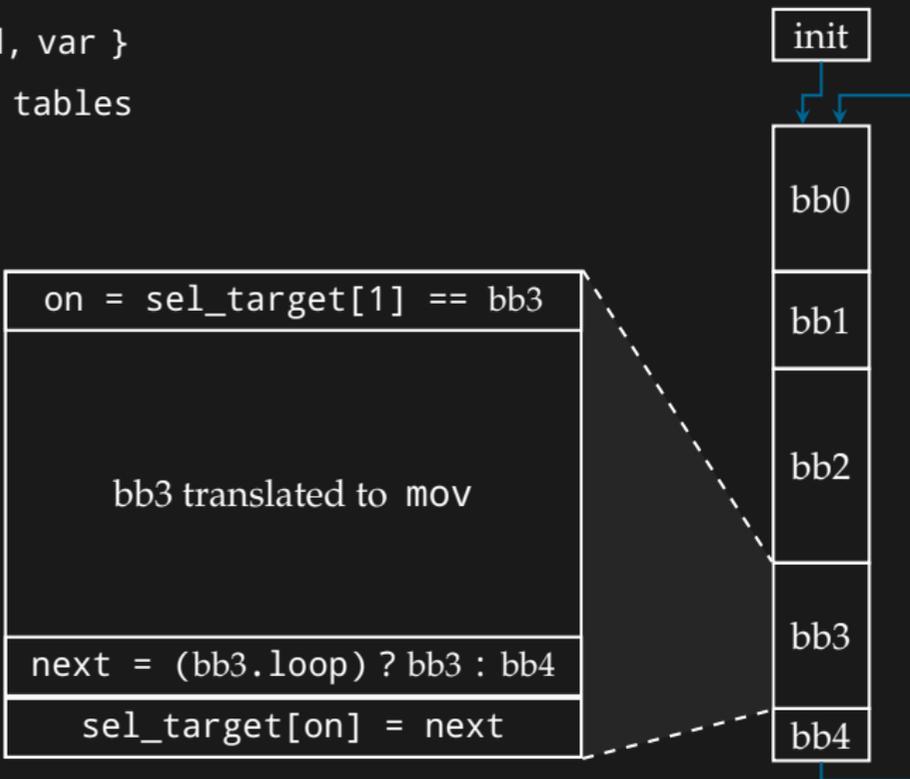
- ▶ `sigaction(SIGSEGV, {&dispatch, 0, 0, 0}, NULL);`
→ **external** library calls
- ▶ `sigaction(SIGILL, {&bb0, 0, 0, 0}, NULL);`
→ **jump** from bb4 to bb0



```
1 #define DEFVAR(TYPE, NAME)      TYPE NAME[2] = { 0 }
2 #define ASSIGN(VAR, VAL, CONDVAR, CONDNUM) \
3   do { VAR[TRUVAL(CONDVAR) == CONDNUM] = VAL; } while (0)
4 int main(int argc, char **argv)
5 {
6   DEFVAR(size_t, state); DEFVAR(size_t, cmp);
7   DEFVAR(uint64_t, fac);
8   DEFVAR(size_t, i); DEFVAR(size_t, j);
9   DEFVAR(void *, my_printf); DEFVAR(void *, my_exit);
10  RESOLV(my_printf, "printf"); RESOLV(my_exit, "exit");
11
12  do {
13    ASSIGN(i,      1,          state, 0);
14    ASSIGN(j,     atoi(argv[1]), state, 0);
15    ASSIGN(fac,   1,          state, 0);
16    ASSIGN(state, 1,          state, 0);
17    ASSIGN(fac,   TRUVAL(fac) * TRUVAL(i), state, 1);
18    ASSIGN(i,     TRUVAL(i) + 1, state, 1);
19    ASSIGN(cmp,   TRUVAL(i) > TRUVAL(j), state, 1);
20    ASSIGN(state, 2,          cmp,   1);
21
22    my_printf[TRUVAL(state) == 2]("%llu\n", TRUVAL(fac));
23    my_exit[TRUVAL(state) == 2](0);
24  } while (1);
25 }
```



- ▶ `sel_var := { discard, var }`
- ▶ **Arithmetic** → lookup tables

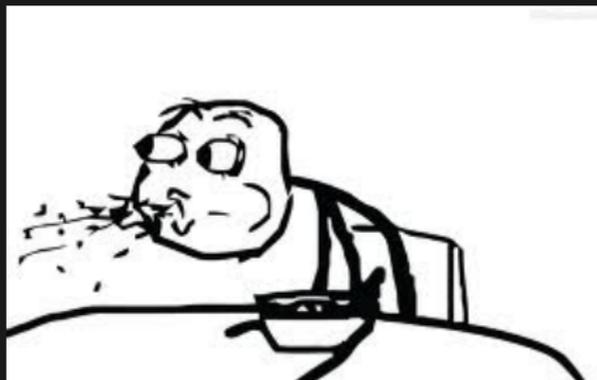


sample program	movfused		lcc + as + ld	
	time [ms]	size	time [ms]	size
for_loop	0.99	5.67 MiB	0.77	3.05 KiB
primes (100)	3.50	5.71 MiB	0.78	3.58 KiB
tiny-aes128	2591	6.28 MiB	1.18	12.7 KiB
sha2-256	81425	5.87 MiB	28.8	6.13 KiB

sample program	movfused		lcc + as + ld	
	time [ms]	size	time [ms]	size
for_loop	0.99	5.67 MiB	0.77	3.05 KiB
primes (100)	3.50	5.71 MiB	0.78	3.58 KiB
tiny-aes128	2591	6.28 MiB	1.18	12.7 KiB
sha2-256	81425	5.87 MiB	28.8	6.13 KiB



CTF	Challenge
Hackover CTF 2015	move_it
0ctf 2016	momo
Google CTF 2016	guessme



Movfuscator version	Language	Generated Code	Easy solution?
movfuscator1	BF	static	✓
movfuscator2	C	static	✓
movfuscator2 + hardening	C	shuffled	!



Chapter 2

The Demovfuscator

Goals:

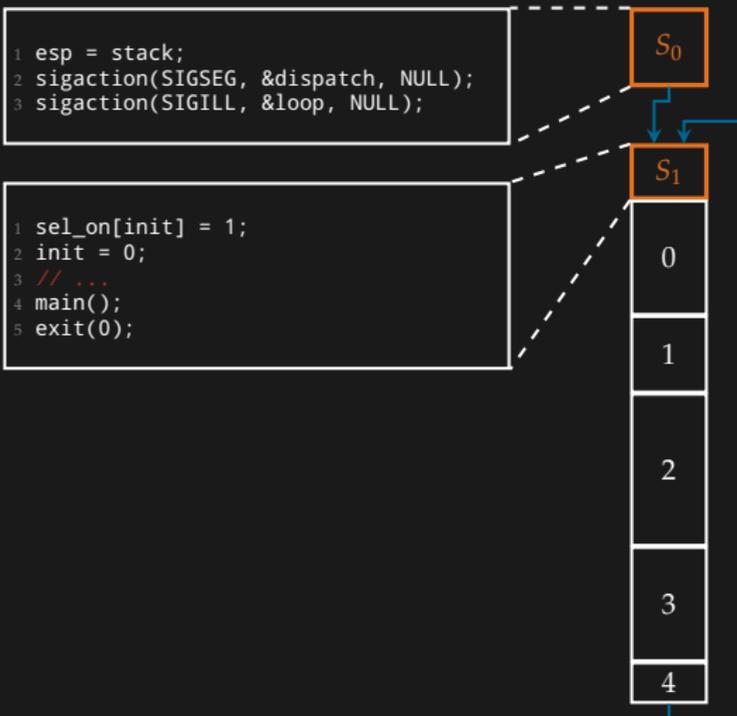
1. Recovery of the control flow
2. Recovery of Symbols
3. Substitution of lookups

4 Stages:

1. Analyzing the setup
2. Recovering labels
3. Finding jump targets
4. Building the CFG

Analyzing the setup:

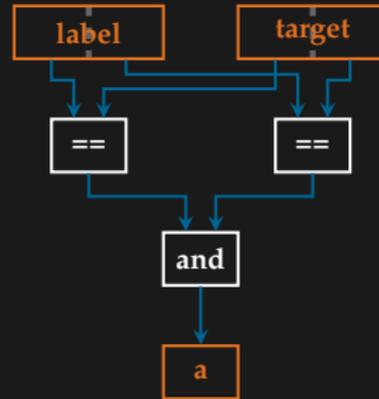
- ▶ Signal handler - main loop
- ▶ Stack setup
- ▶ Initializing ON
- ▶ excepted from hardening



Recovering the Labels:

Analyzing accesses to sel_on:

```
1 a = label == target;  
2 sel_on[a] = 1;
```



Beating lookup tables:

Boolean:

- ▶ set bits according to the result
- ▶ look up the result

operation	value
and	0x8
or	0xE
exclusive or (xor)	0x6
equality (xnor)	0x9

Binary:

- ▶ accessing [0x7][0xCB]
- ▶ look up the result

name	value
bit set	0xCB
bit clear	0x4B
and	0x3
or	0xCF
xor	0xCC
mul_l	0x8D
mul_h	0x5

Unary:

- ▶ hashing the tables
- ▶ comparing the **hashes**

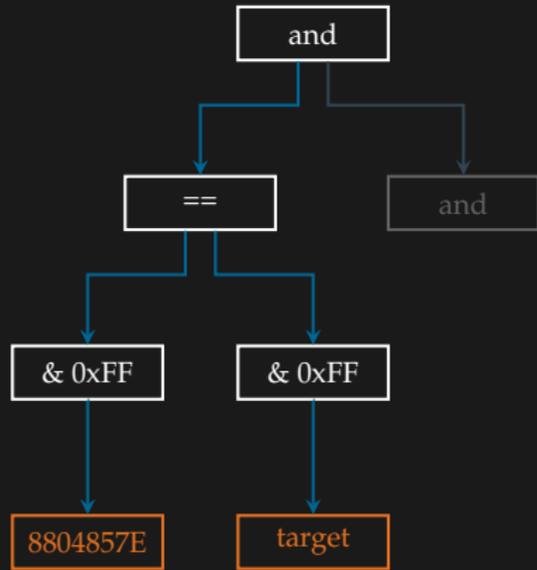
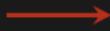


```
mov     eax, dword_83F55B8
mov     edx, 8804857Eh
mov     dword_81F5440, eax
mov     dword_81F5444, edx
mov     eax, 0
mov     ecx, 0
mov     edx, 0
mov     al, byte ptr dword_81F5440
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444
mov     dl, [ecx+edx]
mov     dword_81F5430, edx
mov     al, byte ptr dword_81F5440+1
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444+1
mov     dl, [ecx+edx]
mov     dword_81F5434, edx
:
mov     eax, dword_81F5430
mov     edx, dword_81F5434
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax
:
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax
```

```

mov     eax, dword_83F55B8
mov     edx, 8804857Eh
mov     dword_81F5440, eax
mov     dword_81F5444, edx
mov     eax, 0
mov     ecx, 0
mov     edx, 0
mov     al, byte ptr dword_81F5440
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444
mov     dl, [ecx+edx]
mov     dword_81F5430, edx
mov     al, byte ptr dword_81F5440+1
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444+1
mov     dl, [ecx+edx]
mov     dword_81F5434, edx
:
mov     eax, dword_81F5430
mov     edx, dword_81F5434
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax
:
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax

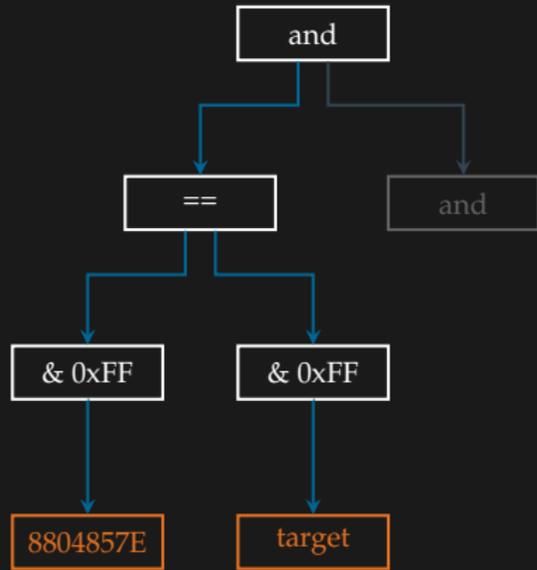
```



```

mov     eax, dword_83F55B8
mov     edx, 8804857Eh
mov     dword_81F5440, eax
mov     dword_81F5444, edx
mov     eax, 0
mov     ecx, 0
mov     edx, 0
mov     al, byte ptr dword_81F5440
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444
mov     dl, [ecx+edx]
mov     dword_81F5430, edx
mov     al, byte ptr dword_81F5440+1
mov     ecx, off_804FA50[eax*4]
mov     dl, byte ptr dword_81F5444+1
mov     dl, [ecx+edx]
mov     dword_81F5434, edx
:
mov     eax, dword_81F5430
mov     edx, dword_81F5434
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax
:
mov     eax, off_804C4F0[eax*4]
mov     eax, [eax+edx*4]
mov     dword_81F5430, eax

```



Z3

Identifying jump targets:

Jump / Call:

```
1 tar_ptr = sel_tar[on];  
2 *tar_ptr = label;
```

Return:

```
1 x = *stack_ptr;  
2 stack_ptr++;  
3 tar_ptr = sel_tar[on];  
4 *tar_ptr = x;
```

Conditional Jump:

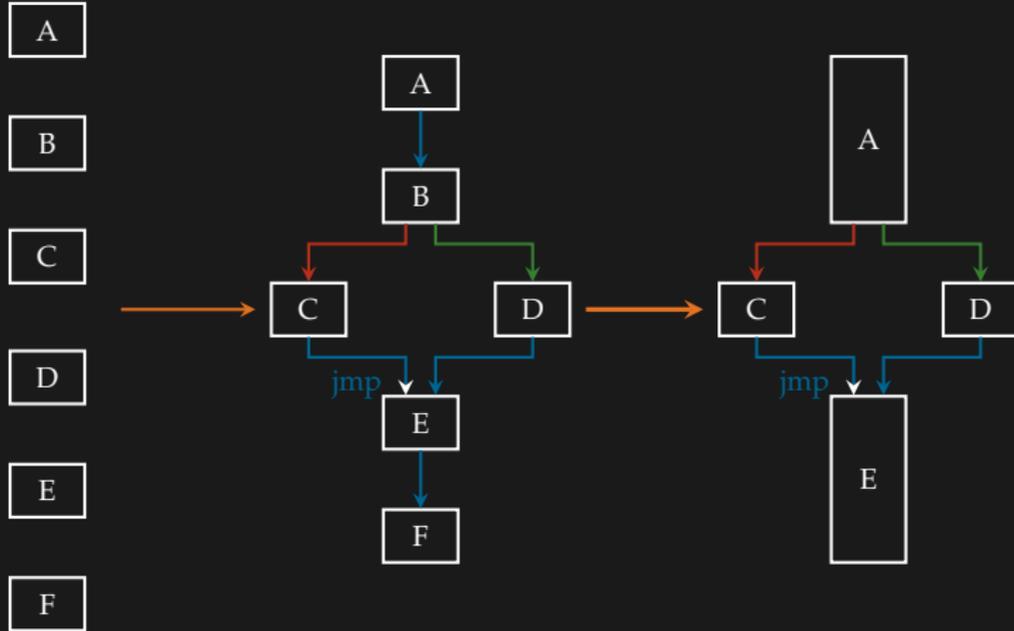
```
1 tar_ptr = sel_tar[condition];  
2 *tar_ptr = label;
```

Indirect jump:

```
1 x = ...  
2 tar_ptr = sel_tar[on]  
3 *tar_ptr = x
```

remember targets on toggling execution off

Generating the Graph:



“You don’t remove all the movs!”

Yes, but demov is able to ...

- ▶ handle **hardened** executables.
- ▶ reconstruct **functions** and their **CFGs**.
- ▶ generate a **patched** binary.
- ▶ generate **symbols** for IDA.
- ▶ perform partial instruction **re-substitution**.

→ Makes reversing moved binaries **much** easier



Simple crackme:

```
1 #include <stdio.h>
2 #include <stdint.h>
3 #include <string.h>
4
5 int main(int argc, char **argv)
6 {
7     char sol[0x20];
8     static int res;
9
10    fgets(sol, sizeof(sol), stdin);
11    sol[12] = 0;
12
13    sol[0] ^= 0x01; sol[1] ^= 0x23; sol[2] ^= 0x45; sol[3] ^= 0x67;
14    sol[4] ^= 0x89; sol[5] ^= 0xab; sol[6] ^= 0xcd; sol[7] ^= 0xef;
15    *(uint32_t *)&sol[8] ^= 0xdeadbeef;
16    res = memcmp(sol, "\x31\x5b\x24\x38\xfb\xce\xae\x80\x81\xd3\xd9\xb2", 12);
17    if (res)
18        puts(":(");
19    else
20        puts(":)");
21 }
```

Simple crackme:

```
1 #include <stdio.h>
2 #include <stdint.h>
3 #include <string.h>
4
5 int main(int argc, char **argv)
6 {
7     char sol[0x20];
8     static int res;
9
10    fgets(sol, sizeof(sol), stdin);
11    sol[12] = 0;
12
13    sol[0] ^= 0x01; sol[1] ^= 0x23; sol[2] ^= 0x45; sol[3] ^= 0x67;
14    sol[4] ^= 0x89; sol[5] ^= 0xab; sol[6] ^= 0xcd; sol[7] ^= 0xef;
15    *(uint32_t *)&sol[8] ^= 0xdeadbeef;
16    res = memcmp(sol, "\x31\x5b\x24\x38\xfb\xce\xae\x80\x81\xd3\xd9\xb2", 12);
17    if (res)
18        puts(":(");
19    else
20        puts(":)");
21 }
```



<http://angr.io>

Simple crackme:

- Time to find correct input to reach the statement in line 20 using angr:

Vanilla	Movfuscated	De-Movfuscated
< 1s	> 24h (!)	19s

⇒ Lookup tables are a **huge** problem for symbolic execution

- ▶ **Julian:**
mail [at] kirschju.re
F949 CFBD 140A 6DD0 71E9 0B8C DC24 396B 6D45 1038
- ▶ **Clemens:**
jonischk [at] cs.tum.edu
A903 76D1 65F3 25F9 8594 280A 2BA0 1592 EFAC B551
- ▶ **Sources** available – documentation pending :-)
 - **Source code:**
<https://github.com/kirschju/demovfuscator>
 - **Project website:**
<https://kirschju.re/demov>
 - **Clemens' thesis:**
https://kirschju.re/static/ba_jonischkeit_2016.pdf

Thanks!